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UNIVERSAL FLASH STORAGE (UFS), VERSION 2.0

CONTENTS

		Page
1	Scope	1
2	Normative Reference	1
3	Terms and Definitions	2
4	Introduction	7
4.1	General Features	7
4.2	Interface Features	8
4.3	Functional Features	8
5	UFS Architecture Overview	9
5.1	UFS Top level Architecture	9
5.2	UFS System Model	12
5.3	System Boot and Enumeration	12
5.4	UFS Interconnect (UIC) Layer	13
5.4.1	UFS Physical Layer Signals	13
5.4.2	MIPI UniPro	13
5.4.3	MIPI UniPro Related Attributes	14
5.5	UFS Transport Protocol (UTP) Layer	14
5.5.1	Architectural Model	15
5.6	UFS Application and Command Layer	19
6	UFS Electrical: Clock, Reset, Signals and Supplies	20
6.1	Embedded UFS Signals	20
6.2	UFS Memory Card Signals	21
6.3	Reset Signal	22
6.4	Power Supplies	22
6.5	Reference Clock	23
6.5.1	HS Gear Rates	24
6.5.2	Host Controller requirements for reference clock generation	25
6.6	External Charge Pump Capacitors (Optional)	26
6.7	Absolute Maximum DC Ratings	27
7	Reset, Power-up and Power-down	28
7.1	Reset	28
7.1.1	Power-on Reset	28
7.1.2	Hardware Reset	29
7.1.3	EndPointReset	30
7.1.4	Logical Unit Reset	31
7.1.5	Other Resets	31
7.1.6	Summary of Resets and Device Behavior	32
7.2	Power up ramp	33

7.3	Power off ramp	34
7.4	UFS Device Power Modes and LU Power Condition	35
7.4.1	Device Power Modes	35
7.4.2	Power Management Command: START STOP UNIT	43
7.4.3	Power Mode Control	45
7.4.4	Logical Unit Power Condition	47
8	UFS UIC Layer: MIPI M-PHY	48
8.1	Termination	48
8.2	Drive Levels	48
8.3	PHY State machine	48
8.4	HS Burst	49
8.4.1	HS Prepare Length Control	49
8.4.2	HS Sync Length Control	49
8.4.3	Slew Rate Control	49
8.5	PWM Burst	49
8.5.1	LS Prepare Length Control	49
8.6	UFS PHY Attributes	49
8.7	Operation Timings	52
8.7.1	Reference Clock Timings	52
8.8	Electrical characteristics	52
8.8.1	Transmitter Characteristics	52
8.8.2	Receiver Characteristics	52
9	UFS UIC Layer: MIPI Unipro	53
9.1	Overview	53
9.2	Architectural Model	53
9.3	UniPro/UFS Transport Protocol Interface (Data Plane)	54
9.4	UniPro/UFS Control Interface (Control Plane)	55
9.5	UniPro/UFS Transport Protocol Address Mapping	56
9.6	Options and Tunable Parameters of UniPro	57
9.6.1	UniPro PHY Adapter	57
9.6.2	UniPro Data Link Layer	57
9.6.3	UniPro Network Layer	57
9.6.4	UniPro Transport Layer	57
9.6.5	UniPro Device Management Entity Transport Layer	58
9.6.6	UniPro Attributes	59
10	UFS Transport Protocol (UTP) Layer	60
10.1	Overview	60
10.2	UTP and UniPro Specific Overview	61
10.2.1	Phases	61
10.2.2	Data Pacing	61
10.2.3	UniPro	61
10.3	UFS Transport Protocol Transactions	62
10.3.1	Overview	62
10.4	Service Delivery Subsystem	62

10.4.1	UPIU Transaction Codes	62
10.5	General UFS Protocol Information Unit Format	64
10.5.1	Overview	65
10.5.2	Basic Header Format	65
10.5.3	COMMAND UPIU	70
10.5.4	RESPONSE UPIU	73
10.5.5	DATA OUT UPIU	81
10.5.6	DATA IN UPIU	83
10.5.7	READY TO TRANSFER UPIU	85
10.5.8	TASK MANAGEMENT REQUEST UPIU	87
10.5.9	TASK MANAGEMENT RESPONSE UPIU	89
10.5.10	QUERY REQUEST UPIU	91
10.5.11	QUERY RESPONSE UPIU	104
10.5.12	REJECT UPIU	116
10.5.13	NOP OUT UPIU	118
10.5.14	NOP IN UPIU	120
10.6	Logical Units	122
10.6.1	Overview	122
10.6.2	UFS SCSI Domain	122
10.6.3	UFS Logical Unit Definition	122
10.6.4	Well-Known Logical Unit Definition	123
10.6.5	Logical Unit Addressing	123
10.6.6	Well Known Logical Unit Defined in UFS	124
10.6.7	Translation of 8-bit UFS LUN to 64-bit SCSI LUN Address	125
10.6.8	SCSI Write Command	126
10.6.9	SCSI Read Command	127
10.7	Application Layer and Device Manager Transport Protocol Services	128
10.7.1	UFS Initiator Port and Target Port Attributes	128
10.7.2	Execute Command procedure call transport protocol services	128
10.7.3	Send SCSI Command transport protocol service	130
10.7.4	SCSI Command Received transport protocol	131
10.7.5	Send Command Complete transport protocol service	132
10.7.6	Command Complete Received transport protocol service	133
10.7.7	Data transfer SCSI transport protocol services	133
10.7.8	Task Management Function procedure calls	140
10.7.9	Query Function transport protocol services	145
11	UFS Protocol layer – SCSI Commands	149
11.1	Universal Flash Storage Command Layer (UCL) Introduction	149
11.1.1	The Command Descriptor Block (CDB)	149
11.2	Universal Flash Storage Native Commands (UNC)	149
11.3	Universal Flash Storage SCSI Commands	150
11.3.1	General information about SCSI commands in UFS	151
11.3.2	INQUIRY Command	151
11.3.3	MODE SELECT (10) Command	154

11.3.4	MODE SENSE (10) Command	156
11.3.5	READ (6) Command	159
11.3.6	READ (10) Command	160
11.3.7	READ (16) Command	162
11.3.8	READ CAPACITY (10) Command	164
11.3.9	READ CAPACITY (16) Command	166
11.3.10	START STOP UNIT	170
11.3.11	TEST UNIT READY Command	171
11.3.12	REPORT LUNS Command	172
11.3.13	VERIFY (10)	177
11.3.14	WRITE (6) Command	179
11.3.15	WRITE (10) Command	181
11.3.16	WRITE (16) Command	184
11.3.17	REQUEST SENSE Command	187
11.3.18	FORMAT UNIT Command	191
11.3.19	PRE-FETCH (10) Command	193
11.3.20	PRE-FETCH (16) Command	196
11.3.21	SECURITY PROTOCOL IN Command	197
11.3.22	SECURITY PROTOCOL OUT Command	199
11.3.23	SEND DIAGNOSTIC Command	201
11.3.24	SYNCHRONIZE CACHE (10) Command	203
11.3.25	SYNCHRONIZE CACHE (16) Command	206
11.3.26	UNMAP Command	207
11.3.27	READ BUFFER Command	210
11.3.28	WRITE BUFFER Command	213
11.4	Mode Pages	216
11.4.1	Mode Page Overview	216
11.4.2	UFS Supported Pages	221
12	UFS Security	228
12.1	UFS Security Feature Support Requirements	228
12.2	Secure Mode	228
12.2.1	Description	228
12.2.2	Requirements	229
12.2.3	Implementation	230
12.3	Device Data Protection	235
12.3.1	Description and Requirements	235
12.3.2	Implementation	235
12.4	RPMB	236
12.4.1	Description	236
12.4.2	RPMB Well Known Logical Unit Description	236
12.4.3	Requirements	237
12.4.4	Implementation	242
12.4.5	SECURITY PROTOCOL IN/OUT Commands	243
12.4.6	RPMB Operations	248

12.5	Malware Protection	260
12.6	Mechanical	260
13	UFS FUNCTIONAL DESCRIPTIONS	261
13.1	UFS Boot	261
13.1.1	Introduction	261
13.1.2	Boot Configuration	261
13.1.3	Initialization and boot code download process	264
13.1.4	Initialization process without boot code download	267
13.1.5	Boot Logical Unit Operations	268
13.1.6	Configurability	268
13.1.7	Security	269
13.2	Logical Unit Management	269
13.2.1	Introduction	269
13.2.2	Logical Unit features	269
13.2.3	Logical Unit Configuration	272
13.3	Logical Block Provisioning	275
13.4	Host Device Interaction	276
13.4.1	Overview	276
13.4.2	Applicable Devices	276
13.4.3	Command Queue: Inter-LU Priority	276
13.4.4	Background Operations Mode	277
13.4.5	Power Off Notification	279
13.4.6	Dynamic Device Capacity	280
13.4.7	Data Reliability	284
13.4.8	Real-Time Clock Information	285
13.4.9	Context Management	286
13.4.10	System Data Tag Mechanism	290
13.4.11	Exception Events Mechanism	291
13.4.12	Data transfer rules related with RTT (Ready-to-Transfer)	292
13.5	UFS Cache	297
14	UFS Descriptors, Flags and Attributes	299
14.1	UFS Descriptors	299
14.1.1	Descriptor Types	299
14.1.2	Descriptor Indexing	300
14.1.3	Accessing Descriptors	300
14.1.4	Read Descriptor	301
14.1.5	Write Descriptor	302
14.1.6	Descriptor Page Definitions	303
14.2	Flags	328
14.3	Attributes	333
15	Annex A - Dynamic Capacity Host Implementation Example	342
15.1	Overview	342
15.2	Method Outline	342

FIGURES

Figure 5.1: UFS Top Level Architecture	9
Figure 5.2: Usage of UDM_SAP	10
Figure 5.3: Usage of UIO_SAP	10
Figure 5.4: UFS System Model	12
Figure 5.5: SCSI Domain Class Diagram	16
Figure 5.6: UFS Domain Class Diagram	17
Figure 6.1: UFS Device Block Diagram	20
Figure 6.2: Clock input levels, rise time, and fall time	24
Figure 6.4: Output driver and Input receiver levels	25
Figure 6.5: Clock output levels, rise time and fall time	26
Figure 7.1: Power-on Reset.....	28
Figure 7.2: Hardware Reset	29
Figure 7.3: Reset AC timings.....	29
Figure 7.4: EndPointReset	30
Figure 7.5: Logical Unit Reset.....	31
Figure 7.6: Power up ramps	33
Figure 7.7: Power off ramps	34
Figure 7.8: Power Mode State Machine.....	39
Figure 8.1: Simplified example for I/O termination	48
Figure 9.1: UniPro internal layering view (left) and UniPro Black Box view (right)	53
Figure 10.1: UFS SCSI domain	122
Figure 10.2: Logical Unit Addressing.....	123
Figure 10.3: SCSI Write	126
Figure 10.4: SCSI Read	127
Figure 10.5: Command w/o Data Phase.....	129
Figure 10.6: Command + Read Data Phase 1/2	135
Figure 10.7: Command + Read Data Phase 2/2	136
Figure 10.8: Command + Write Data Phase 1/2	138
Figure 10.9: Command + Write Data Phase 2/2	139
Figure 10.10: Task Management Function	143
Figure 10.11: UFS Query Function.....	146
Figure 11.1: UFS Command Layer.....	149
Figure 12.1: Purge operation state machine.....	232
Figure 12.2: Authentication Key Programming Flow.....	255
Figure 12.3: Read Counter Value Flow	256
Figure 12.4: Authenticated Data Write Flow.....	258
Figure 12.5: Authenticated Read Flow	260
Figure 13.1: UFS System Diagram.....	261
Figure 13.2: Example of UFS Device Memory Organization for Boot	263
Figure 13.3: Device Initialization and Boot Procedure Sequence Diagram	266
Figure 13.4: Example of UFS Device Memory Organization	270
Figure 13.5: Physical Memory Resource State Machine	283

Figure 13.6: Example of data status after a power failure during reliable write operation.....	284
Figure 13.7: Host-Device interaction example for Rule-1	292
Figure 13.8: Host-Device interaction example for Data Out Mismatch Error case	293
Figure 13.9: Host-Device interaction example for Rule-2.....	293
Figure 13.10: Host-Device interaction example for Rule-3.....	294
Figure 13.11: Host-Device interaction for LU with same the priority.....	295
Figure 13.12: Host-Device interaction for LU with different priorities.....	295
Figure 14.1: Descriptor Organization	300
Figure 14.2: Read Request Descriptor	301
Figure 14.3: Write Request Descriptor	302

TABLES

Table 5.1: UFS Signals	13
Table 5.2: ManufacturerID and DeviceClass Attributes.....	14
Table 6.1: Signal Name and Definitions	21
Table 6.2: Reset Signal Electrical Parameters	22
Table 6.3: UFS Supply Voltages.....	22
Table 6.4: Voltage configurations for Embedded UFS.....	22
Table 6.5: Voltage configurations for UFS Card.....	22
Table 6.6: Reference Clock parameters	23
Table 6.7: HS-BURST Rates	24
Table 6.8: Host controller reference clock parameters	25
Table 6.9: Charge pump capacitors description.....	26
Table 6.10: Charge pump related ball names.....	27
Table 6.11: Absolute maximum DC ratings	27
Table 7.1: Reset timing parameters.....	29
Table 7.2: Reset States	32
Table 7.3: UniPro Attributes and Descriptor Reset	32
Table 7.4: Allowed SCSI commands and UPIU for each Power Mode	41
Table 7.5: Device Well Known Logical Unit Responses to SSU command	42
Table 7.6: Device Well Known Logical Unit Responses to commands other than SSU.....	43
Table 7.7: Pollable Sense Data for each Power Modes	43
Table 7.8: START STOP UNIT command.....	43
Table 7.9: START STOP UNIT fields.....	44
Table 7.10: Attribute for Power Mode Control.....	45
Table 7.11: Device Descriptor parameters.....	45
Table 7.12: Power Parameters Descriptor fields.....	46
Table 7.13: Format for Power Parameter element	46
Table 7.14: Logical Unit Response to SCSI command.....	47
Table 8.1: UFS PHY M-TX Capability Attributes	50
Table 8.2: UFS PHY M-RX Capability Attributes	51

Table 9.1: DME Service Primitives	58
Table 9.2: UniPro Attribute	59
Table 10.1: UPIU Transaction Codes	62
Table 10.2: UPIU Transaction Code Definitions.....	63
Table 10.3: General format of the UFS Protocol Information Unit	64
Table 10.4: Basic Header Format	65
Table 10.5: Transaction Type Format.....	65
Table 10.6: UPIU Flags	66
Table 10.7: Task Attribute definition.....	66
Table 10.8: UTP Response Values	67
Table 10.9: UPIU associated to a single task.....	67
Table 10.10: Command Set Type	68
Table 10.11: COMMAND UPIU.....	70
Table 10.12: Flags Definition for COMMAND UPIU	71
Table 10.13: RESPONSE UPIU	73
Table 10.14: SCSI Status Values	75
Table 10.15: Flags and Residual Count Relationship	77
Table 10.16: SCSI Sense Data Format.....	79
Table 10.17: Sense Key	80
Table 10.18: DATA OUT UPIU	81
Table 10.19: SCSI Data IN UPIU	83
Table 10.20: READY TO TRANSFER UPIU.....	85
Table 10.21: Task Management Request UPIU.....	87
Table 10.22: Task Management Function values	88
Table 10.23: Task Management Input Parameters.....	88
Table 10.24: Task Management Response UPIU	89
Table 10.25: Task Management Output Parameters.....	90
Table 10.26: Task Management Service Response.....	90
Table 10.27: QUERY REQUEST UPIU	91
Table 10.28: Query Function field values.....	93
Table 10.29: Transaction specific fields	94
Table 10.30: Query Function opcode values	94
Table 10.31: Read descriptor	95
Table 10.32: Write Descriptor	96
Table 10.33: Read Attribute.....	97
Table 10.34: Write Attribute.....	98
Table 10.35: Read Flag	99
Table 10.36: Set Flag	100
Table 10.37: Clear Flag.....	101
Table 10.38: Toggle Flag	102
Table 10.39: NOP	103
Table 10.40: QUERY RESPONSE.....	104
Table 10.41: Query Response Code.....	105
Table 10.42: Transaction Specific Fields.....	106

Table 10.43: Query Function opcode values	106
Table 10.44: Read Descriptor	107
Table 10.45: Write Descriptor	108
Table 10.46: Read Attribute Response Data Format	109
Table 10.47: Write Attribute.....	110
Table 10.48: Read Flag Response Data Format.....	111
Table 10.49: Set Flag.....	112
Table 10.50: Clear Flag.....	113
Table 10.51: Toggle Flag.....	114
Table 10.52: NOP	115
Table 10.53: Reject UPIU	116
Table 10.54: Basic Header Status Description.....	117
Table 10.55: E2E Status Definition	117
Table 10.56: NOP OUT UPIU	118
Table 10.57: NOP IN UPIU.....	120
Table 10.58: Well known logical unit commands	124
Table 10.59: Examples of logical unit representation format	125
Table 10.60: UFS Initiator Port and Target Port Attributes.....	128
Table 10.61: Send SCSI Command transport protocol service.....	130
Table 10.62: SCSI Command Received transport protocol.....	131
Table 10.63: Send Command Complete transport protocol service.....	132
Table 10.64: Command Complete Received transport protocol service.....	133
Table 10.65: Send Data-In transport protocol service	134
Table 10.66: Data-In Delivered transport protocol service.....	134
Table 10.67: Receive Data-Out transport protocol service	137
Table 10.68: Data-Out Received transport protocol service	137
Table 10.69: Task Management Function procedure calls	140
Table 10.70: SCSI transport protocol service responses.....	140
Table 10.71: Send Task Management Request SCSI transport protocol service request	144
Table 10.72: Task Management Request Received SCSI transport protocol service indication	144
Table 10.73: Task Management Function Executed SCSI transport protocol service response.....	144
Table 10.74: Received Task Management Function Executed SCSI transport protocol service confirmation.....	145
Table 10.75: Send Query Request UFS transport protocol service	145
Table 10.76: Query Request Received UFS transport protocol service indication.....	147
Table 10.77: Query Function Executed UFS transport protocol service response	147
Table 10.78: Received Query Function Executed UFS transport protocol service confirmation.....	148
Table 11.1: UFS SCSI Command Set.....	150
Table 11.2: INQUIRY command.....	151
Table 11.3: Standard INQUIRY data format	152
Table 11.4: Inquiry Response Data.....	153
Table 11.5: MODE SELECT (10) Command.....	154
Table 11.6: Mode Select Command Parameters	155
Table 11.7: MODE SENSE (10) Command	157

Table 11.8: Mode Sense Command Parameters	157
Table 11.9: Page Control Function	158
Table 11.10: READ (6) UFS Command	159
Table 11.11: READ (10) UFS Command	160
Table 11.12: READ (16) UFS Command	162
Table 11.13: READ CAPACITY (10)	164
Table 11.14: Read Capacity (10) Parameter Data	165
Table 11.15: READ CAPACITY (16)	166
Table 11.16: Read Capacity (16) Parameter Data	168
Table 11.17: START STOP UNIT command	170
Table 11.18: TEST UNIT READY command	171
Table 11.19: REPORT LUNS command	172
Table 11.20: Report LUNS Command Parameters	173
Table 11.21: Report LUNS Command Select Report Field Values	173
Table 11.22: Report LUNS Parameter Data Format	174
Table 11.23: Single level LUN structure using peripheral device addressing method	174
Table 11.24: Well Known Logical Unit Extended Addressing Format	175
Table 11.25: Format of LUN field in UPIU	176
Table 11.26: Well known logical unit numbers	176
Table 11.27: VERIFY Command Descriptor Block	177
Table 11.28: Verify Command Parameters	178
Table 11.29: WRITE (6) Command Descriptor Block	179
Table 11.30: WRITE (10) Command Descriptor Block	181
Table 11.31: WRITE (16) Command Descriptor Block	184
Table 11.32: REQUEST SENSE Command Descriptor Block	187
Table 11.33: Sense Data	188
Table 11.34: Sense Key Descriptions	189
Table 11.35: FORMAT UNIT Command Descriptor Block	191
Table 11.36: Format Unit Command Parameters	191
Table 11.37: PRE_FETCH Command Descriptor Block	193
Table 11-38: PRE-FETCH Command Parameters	194
Table 11.39: PRE-FETCH (16) Command Descriptor Block	196
Table 11.40: SECURITY PROTOCOL IN command	197
Table 11.41: SECURITY PROTOCOL OUT command	199
Table 11.42: SEND DIAGNOSTIC Command Descriptor Block	201
Table 11.43: Send Diagnostic Parameters	201
Table 11.44: SYNCHRONIZE CACHE Command Descriptor Block	203
Table 11.45: Synchronize Cache Command Parameters	204
Table 11.46: SYNCHRONIZE CACHE (16) Command Descriptor Block	206
Table 11.47: UNMAP Command Descriptor Block	207
Table 11.48: UNMAP parameter list	208
Table 11.49: UNMAP block descriptor	209
Table 11.50: READ BUFFER Command	210
Table 11.51: Read Buffer Command Parameters	211

Table 11.52: Read Buffer Command Mode Field Values.....	211
Table 11.53: WRITE BUFFER Command	213
Table 11.54: Write Buffer Command Parameters.....	214
Table 11.55: Write Buffer Command Mode Field Values.....	214
Table 11.56: Mode page code usage.....	216
Table 11.57: UFS Mode parameter list.....	217
Table 11.58: UFS Mode parameter header (10)	217
Table 11.59: Mode Parameter Header Detail.....	218
Table 11.60: Page_0 mode page format.....	219
Table 11.61: Page 0 Format parameters.....	219
Table 11.62: Sub_page mode page format.....	220
Table 11.63: Subpage Format parameters.....	220
Table 11.64: UFS Supported Pages	221
Table 11.65: Control Mode Page default value	222
Table 11.66: Control Mode Page Parameters	223
Table 11.67: Read-Write Error Recovery Mode Page default value	224
Table 11.68: Read-Write Error Recovery Parameters	225
Table 11.69: Caching Mode Page default value	226
Table 11.70: Caching Mode Page Parameters	227
Table 12.1: RPMB Message Components	238
Table 12.2: Request Message Types.....	239
Table 12.3: Response Message Types	239
Table 12.4: RPMB Operation Result data structure.....	240
Table 12.5: RPMB Operation Results.....	241
Table 12.6: RPMB Message Data Frame.....	242
Table 12.7: CDB format of Security Protocol In/Out commands.....	244
Table 12.8: Security Protocol specific field values.....	245
Table 12.9: Supported security protocols SECURITY PROTOCOL IN parameter data	246
Table 12.10: UFS Supported security protocols SECURITY PROTOCOL IN parameter data.....	246
Table 12.11: Certificate data SECURITY PROTOCOL IN parameter data.....	247
Table 12.12: UFS certificate data SECURITY PROTOCOL IN parameter data	247
Table 12.13: Security Protocol Out command.....	248
Table 12.14: Expected Data Transfer Length value for Request Type Messages	248
Table 12.15: Ready To Transfer	249
Table 12.16: RPMB message data frame.....	249
Table 12.17: RESPONSE UPIU	250
Table 12.18: Response Type Message Delivery: COMMAND UPIU	251
Table 12.19: Expected Data Transfer Length value for Response Type Messages.....	251
Table 12.20: Response Type Message Delivery: DATA IN UPIU	252
Table 12.21: Response Type Message Delivery: RESPONSE UPIU.....	253
Table 13.1: bBootLunEn Attribute	262
Table 13.2: Valid UPIUs and SCSI Commands for Each Initialization Phase	267
Table 13.3: Logical unit configurable parameters	272
Table 13.4: Parameter for controlling logical unit data reliability.....	285

Table 13.5: Parameters for implementing RTT Rules	296
Table 14.1: Descriptor identification values	299
Table 14.2: Generic Descriptor Format	303
Table 14.3: Logical Unit Descriptor Format.....	303
Table 14.4: Device Descriptor	304
Table 14.5: wManufacturerID definition	309
Table 14.6: Configuration Descriptor Format.....	310
Table 14.7: Configuration Descr. Header and Device Descr. Conf. parameters	311
Table 14.8: Unit Descriptor configurable parameters.....	312
Table 14.9: Geometry Descriptor.....	313
Table 14.10: Unit Descriptor	319
Table 14.11: RPMB Unit Descriptor	322
Table 14.12: Power Parameters Descriptor.....	324
Table 14.13: Interconnect Descriptor.....	325
Table 14.14: Manufacturer Name String	325
Table 14.15: Product Name String.....	326
Table 14.16: OEM_ID String	326
Table 14.17: Serial Number String Descriptor	327
Table 14.18: Flags access properties.....	328
Table 14.19: Flags.....	329
Table 14.20: Attributes access properties	333
Table 14.21: Attributes	334

Foreword

This standard has been prepared by JEDEC. The purpose of this standard is definition of an UFS Universal Flash Storage electrical interface and an UFS memory device. This standard defines a unique UFS feature set and includes the feature set of eMMC Specification as a subset. This standard references several other standard specifications by MIPI (M-PHY and UniPro Specifications) and INCITS T10 (SBC, SPC and SAM draft standards) organizations.

Introduction

The UFS electrical interface is a universal serial communication bus which can be utilized for different type of applications. It's based on MIPI M-PHY standard as physical layer for optimized performance and power. Architectural model references the INCITS T10 SAM model for ease of adoption.

The UFS device is a universal data storage and communication media. It is designed to cover a wide area of applications as smart phones, cameras, organizers, PDAs, digital recorders, MP3 players, internet tablets, electronic toys, etc.

UNIVERSAL FLASH STORAGE (UFS), VERSION 2.0

(From JEDEC Board Ballot JCB-13-45, formulated under the cognizance of the JC-64.1 Committee on Electrical Specifications and Command Protocols)

1 SCOPE

This standard specifies the characteristics of the UFS electrical interface and the memory device. Such characteristics include (among others) low power consumption, high data throughput, low electromagnetic interference and optimization for mass memory subsystem efficiency. The UFS electrical interface is based on an advanced differential interface by MIPI M-PHY standard which together with the MIPI UniPro standard forms the interconnect of the UFS interface. The architectural model is referencing the INCITS T10 (SCSI) SAM standard and the command protocol is based on INCITS T10 (SCSI) SPC and SBC standards.

2 NORMATIVE REFERENCE

The following normative documents contain provisions that, through reference in this text, constitute provisions of this standard. For dated references, subsequent amendments to, or revisions of, any of these publications do not apply. However, parties to agreements based on this standard are encouraged to investigate the possibility of applying the most recent editions of the normative documents indicated. For undated references, the latest edition of the normative document referred to applies.

[MIPI-M-PHY], *MIPI Alliance Specification for M-PHYSM, Version 3.0*

[MIPI-UniPro], *MIPI Alliance Specification for Unified Protocol (UniProSM), Version 1.6*

[MIPI-DDB], *MIPI Alliance Specification for Device Descriptor Block (DDB), Version*

[SAM], *INCITS T10 draft standard: SCSI Architecture Model – 5 (SAM–5), Revision 05, 19 May 2010*

[SPC], *INCITS T10 draft standard: SCSI Primary Commands – 4 (SPC-4), Revision 27, 11 October 2010*

[SBC], *INCITS T10 draft standard: SCSI Block Commands – 3 (SBC–3), Revision 24, 05 August 2010*