

JEDEC STANDARD

Lead Integrity

JESD22-B105E
(Revision of JESD22-B105D, July 2011)

FEBRUARY 2018

JEDEC SOLID STATE TECHNOLOGY ASSOCIATION



NOTICE

JEDEC standards and publications contain material that has been prepared, reviewed, and approved through the JEDEC Board of Directors level and subsequently reviewed and approved by the JEDEC legal counsel.

JEDEC standards and publications are designed to serve the public interest through eliminating misunderstandings between manufacturers and purchasers, facilitating interchangeability and improvement of products, and assisting the purchaser in selecting and obtaining with minimum delay the proper product for use by those other than JEDEC members, whether the standard is to be used either domestically or internationally.

JEDEC standards and publications are adopted without regard to whether or not their adoption may involve patents or articles, materials, or processes. By such action JEDEC does not assume any liability to any patent owner, nor does it assume any obligation whatever to parties adopting the JEDEC standards or publications.

The information included in JEDEC standards and publications represents a sound approach to product specification and application, principally from the solid state device manufacturer viewpoint. Within the JEDEC organization there are procedures whereby a JEDEC standard or publication may be further processed and ultimately become an ANSI standard.

No claims to be in conformance with this standard may be made unless all requirements stated in the standard are met.

Inquiries, comments, and suggestions relative to the content of this JEDEC standard or publication should be addressed to JEDEC at the address below, or refer to www.jedec.org under Standards and Documents for alternative contact information.

Published by
©JEDEC Solid State Technology Association 2013
3103 North 10th Street
Suite 240 South
Arlington, VA 22201-2107

This document may be downloaded free of charge; however JEDEC retains the copyright on this material. By downloading this file the individual agrees not to charge for or resell the resulting material.

PRICE: Contact JEDEC

Printed in the U.S.A.
All rights reserved

PLEASE!

DON'T VIOLATE
THE
LAW!

This document is copyrighted by JEDEC and may not be reproduced without permission.

For information, contact:

JEDEC Solid State Technology Association
3103 North 10th Street
Suite 240 South
Arlington, VA 22201-2107

or refer to www.jedec.org under Standards-Documents/Copyright Information.

TEST METHOD B105E

LEAD INTEGRITY

(From JEDEC Board Ballot JCB-17-13, formulated under the cognizance of JC-14.1 Committee on Reliability Test Methods for Packaged Devices.)

1 Scope

This test method provides various test conditions for determining the integrity of the lead/package interface and the lead itself when the lead(s) are bent due to faulty board assembly processing followed by rework of the part for reassembly. For hermetic packages hermeticity testing per JESD22A109 (Test Method A109) is required to identify any adverse effects from the stresses applied to the seals as well as to the leads. All of the test conditions within this test method are considered destructive and are only recommended for qualification testing. This test method is applicable to all through-hole devices and surface-mount devices that require lead forming by the user. The following is a summary of the test conditions in this standard;

a) Test Condition A - Tension

This test condition provides for the application of straight tensile loading. See clause 6.

b) Test Condition B - Bending Stress

This test condition provides for the application of bending stresses to determine the integrity of leads, seals and lead plating. See clause 7.

c) Test Condition C - Lead Fatigue

This test condition provides for the application of bending stresses primarily to determine the resistance of the leads to metal fatigue under repeated bending. See clause 8.

d) Test Condition D - Lead Torque

This test condition provides for the application of stresses to the leads to determine the resistance of seals and leads to twisting motions. See clause 9.

e) Test Condition E - Stud Torque

This test condition provides for the application of stresses on a threaded mounting stud caused by tightening the device during mounting. See clause 10.

2 Normative reference

JESD22-A109, *Hermeticity*.