JEDEC STANDARD

POD125 - 1.25 V Pseudo Open Drain I/O

JESD8-30A

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JEDEC SOLID STATE TECHNOLOGY ASSOCIATION

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POD125 - 1.25 V PSEUDO OPEN DRAIN I/O

(From JEDEC Board Ballot JCB-19-12, formulated under the cognizance of the JC-16 Committee on Interface Technology.)

1 Scope

This standard defines the DC and AC single-ended (data) and differential (clock) operating conditions, I/O impedances, and the termination and calibration scheme for 1.25 V Pseudo Open Drain I/Os.

The 1.25 V Pseudo Open Drain interface, also known as POD125, is primarily used to communicate with GDDR6 SGRAM devices.

Multiple Classes of POD125 are expected to reside within the family of POD125 interfaces in order to accommodate various device and market applications. The various classes standardized within the context of POD125 are documented in the appendices of this document (e.g., POD125/Class A).

The core of this standard documents the subset of values common to all Classes of POD125 and documents specification items left to definition within a specific Class as denoted by CDV which is defined as Class Dependent Value.

The values specific to each particular class of POD125 are found in the annexes. See specific Class tables for further details.

NOTE It does not follow that all specification values defined in a given Class are necessarily different from the matching parameter in other Class within POD125. Multiple Classes may reuse a given specification value if appropriate to the Class requirements.