IEC 60749-4

Edition 2.0 2017-03

INTERNATIONAL STANDARD

NORME INTERNATIONALE

Semiconductor devices – Mechanical and climatic test methods – Part 4: Damp heat, steady state, highly accelerated stress test (HAST)

Dispositifs à semiconducteurs – Méthodes d'essais mécaniques et climatiques – Partie 4: Essai continu fortement accéléré de contrainte de chaleur humide (HAST)

IEC 60749-4:2017-03 (en-fr)

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CONTENTS

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SEMICONDUCTOR DEVICES – MECHANICAL AND CLIMATIC TEST METHODS –

Part 4: Damp heat, steady state, highly accelerated stress test (HAST)

FOREWORD

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International Standard IEC 60749-4 has been prepared by IEC technical committee 47: Semiconductor devices.

This second edition cancels and replaces the first edition published in 2002. This edition constitutes a technical revision.

This edition includes the following significant technical changes with respect to the previous edition:

a) clarification of requirements for temperature, relative humidity and duration detailed in Table 1;
b) recommendations that current limiting resistor(s) be placed in the test set-up to prevent test board or DUT damage;
c) allowance of additional time-to-test delay or return-to-stress delay.

This bilingual version (2019-09) corresponds to the monolingual English version, published in 2017-03.

The text of this standard is based on the following documents:

FDIS	Report on voting
47/2346/FDIS	47/2371/RVD

Full information on the voting for the approval of this International Standard can be found in the report on voting indicated in the above table.

The French version of this standard has not been voted upon.

This document has been drafted in accordance with the ISO/IEC Directives, Part 2.

A list of all parts in the IEC 60749 series, published under the general title *Semiconductor devices – Mechanical and climatic test methods*, can be found on the IEC website.

The committee has decided that the contents of this document will remain unchanged until the stability date indicated on the IEC website under "http://webstore.iec.ch" in the data related to the specific document. At this date, the document will be

- reconfirmed,
- withdrawn,
- replaced by a revised edition, or
- amended.

SEMICONDUCTOR DEVICES – MECHANICAL AND CLIMATIC TEST METHODS –

Part 4: Damp heat, steady state, highly accelerated stress test (HAST)

1 Scope

This part of IEC 60749 provides a highly accelerated temperature and humidity stress test (HAST) for the purpose of evaluating the reliability of non-hermetic packaged semiconductor devices in humid environments.

2 Normative references

The following documents are referred to in the text in such a way that some or all of their content constitutes requirements of this document. For dated references, only the edition cited applies. For undated references, the latest edition of the referenced document (including any amendments) applies.

IEC 60749-5, *Semiconductor devices – Mechanical and climatic test methods – Part 5: Steady state temperature humidity bias life test*

3 Terms and definitions

No terms and definitions are listed in this document.

ISO and IEC maintain terminological databases for use in standardization at the following addresses:

- IEC Electropedia: available at http://www.electropedia.org/
- ISO Online browsing platform: available at http://www.iso.org/obp

4 HAST test – General remarks

The HAST test employs severe conditions of temperature, humidity and bias which accelerate the penetration of moisture through the external protective material (encapsulant or seal) or along the interface between the external protective material and the metallic conductors which pass through it. The stress usually activates the same failure mechanisms as the “85/85” steady-state temperature humidity bias life test, IEC 60749-5. As such the test method may be selected from IEC 60749-5 or from this test method. When both test methods are performed, test results of the 85 °C/85 % RH steady-state temperature humidity bias life test, IEC 60749-5, take priority over HAST.

This test method shall be considered destructive.